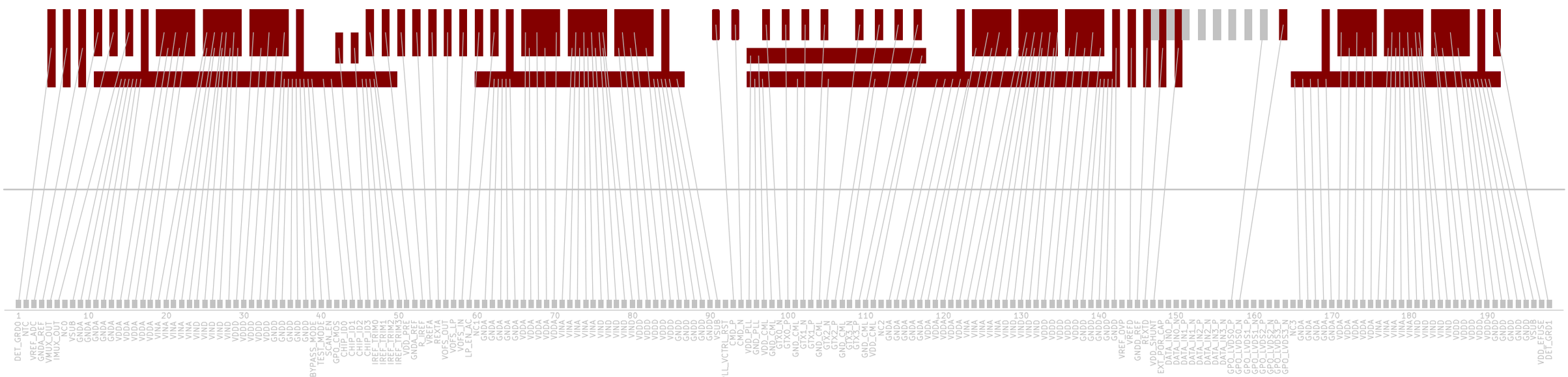
RD53B


1 10 20 30 40 50 60 70 80 90 100 110 120 130 140 150 160 170 180 190
DET_GRD0 NTC VREF_ADC GNDA_REF VMUX_OUT IMUX_OUT NTCG VSUB GNDA GNDA GNDA GNDA GNDA VDDA VDDA VDDA VDDA VINA VINA VINA VINA VINA VIND VIND VIND VIND VIND VDDD VDDD VDDD VDDD VDDD GNDD GNDD GNDD GNDD GNDD BYPASS_MODE TEST_MODE SCAN_EN GPO_CMOS CHIP_ID0 CHIP_ID1 CHIP_ID2 CHIP_ID3 IREF_TRIM0 IREF_TRIM1 IREF_TRIM2 IREF_TRIM3 VDD_PRE GNDA_REF R_REF VREFA VREFD VOFS_OUT VOFS_IP VOFS_IN LP_EN_AC NC1 GNDA GNDA GNDA GNDA GNDA VDDA VDDA VDDA VDDA VDDA VINA VINA VINA VINA VINA VIND VIND VIND VIND VIND VDDD VDDD VDDD VDDD VDDD GNDD GNDD GNDD GNDD GNDD GNDDB VSUB PLL_VCTRL_RST CMD_P CMD_N VDD_PLL GND_PLL VDD_CML GND_CML GIXO_N GIXO_P GND_CML_N GTX1_N GTX1_P GND_CML GTX2_N GTX2_P GND_CML GTX3_N GTX3_P GND_CML VDD_CML NC2 GNDA GNDA GNDA GNDA GNDA VDDA VDDA VDDA VDDA VDDA VINA VINA VINA VINA VINA VIND VIND VIND VIND VIND VDDD VDDD VDDD VDDD VDDD GNDD GNDD GNDD GNDD GNDD GNDDO GNDD GNDD VREFFP VREFFP GNDD_REF REXTD VDD_SHUNT EXT_POR_CAP DATA_IN0_P DATA_IN0_N DATA_IN1_P DATA_IN1_N DATA_IN2_P DATA_IN2_N DATA_IN3_P DATA_IN3_N GPO_LVDS0_P GPO_LVDS0_N GPO_LVDS1_P GPO_LVDS1_N GPO_LVDS2_P GPO_LVDS2_N GPO_LVDS3_P GPO_LVDS3_N NC3 GNDA GNDA GNDA GNDA GNDA VDDA VDDA VDDA VDDA VDDA VINA VINA VINA VINA VINA VIND VIND VIND VIND VIND VDDD VDDD VDDD VDDD VDDD GNDD GNDD GNDD GNDD GNDD VSUB VDD_ERR1 DET_GRD1