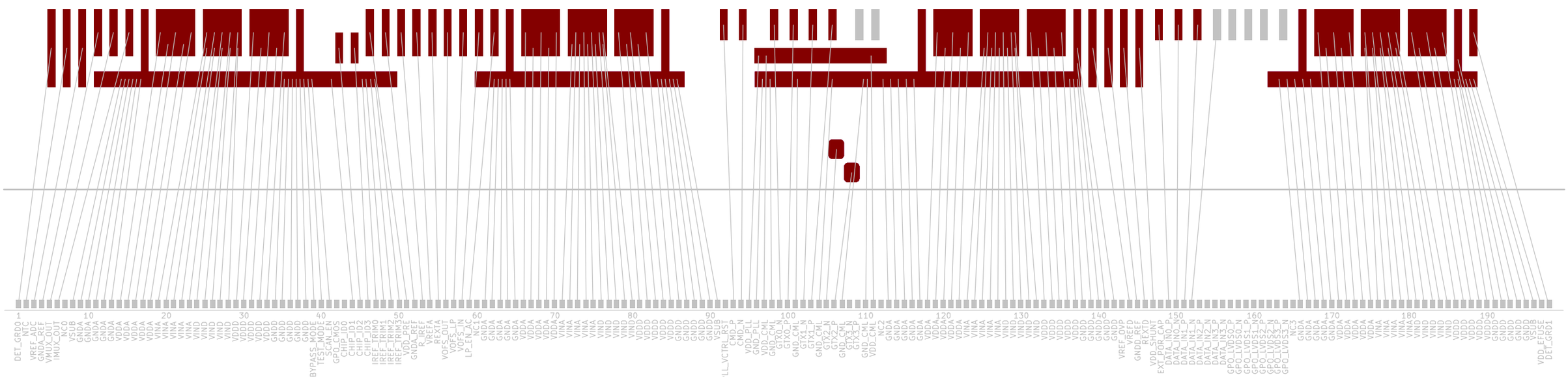
RD53B


1 DET_GRD0
2 NTC
3 VREF_ADC
4 GNDA_REF
5 VMUX_OUT
6 IMUX_OUT
7 NTCG
8 VSUB
9 GNDA
10 GNDA
11 GNDA
12 GNDA
13 VDDA
14 VDDA
15 VDDA
16 VDDA
17 VINA
18 VINA
19 VINA
20 VINA
21 VIND
22 VIND
23 VIND
24 VIND
25 VDDD
26 VDDD
27 VDDD
28 VDDD
29 VDDD
30 GNDD
31 GNDD
32 GNDD
33 GNDD
34 GNDD
35 BYPASS_MODE
36 TEST_MODE
37 SCAN_EN
38 GPO_CMOS
39 CHIP_ID0
40 CHIP_ID1
41 CHIP_ID2
42 CHIP_ID3
43 IREF_TRIM0
44 IREF_TRIM1
45 IREF_TRIM2
46 IREF_TRIM3
47 VDD_PRE
48 GNDA_REF
49 R_REF
50 VREFA
51 VREFD
52 VOFS_OUT
53 VOFS_IP
54 VOFS_IN
55 LP_EN_AC
56 NC1
57 GNDA
58 GNDA
59 GNDA
60 GNDA
61 GNDA
62 VDDA
63 VDDA
64 VDDA
65 VDDA
66 VINA
67 VINA
68 VINA
69 VINA
70 VINA
71 VIND
72 VIND
73 VIND
74 VIND
75 VDDD
76 VDDD
77 VDDD
78 VDDD
79 VDDD
80 GNDD
81 GNDD
82 GNDD
83 GNDD
84 GNDD
85 VSUB
86 PLL_VCTRL_RST
87 CMD_P
88 CMD_N
89 VDD_PLL
90 GND_PLL
91 VDD_CML
92 GND_CML
93 GIXO_P
94 GIXO_N
95 GND_CML
96 GIX1_N
97 GIX1_P
98 GND_CML
99 GTX2_N
100 GTX2_P
101 GND_CML
102 GTX3_N
103 GTX3_P
104 GND_CML
105 VDD_CML
106 NC2
107 GNDA
108 GNDA
109 GNDA
110 GNDA
111 GNDA
112 VDDA
113 VDDA
114 VDDA
115 VDDA
116 VDDA
117 VINA
118 VINA
119 VINA
120 VINA
121 VINA
122 VIND
123 VIND
124 VIND
125 VIND
126 VDDD
127 VDDD
128 VDDD
129 VDDD
130 VDDD
131 GNDD
132 GNDD
133 GNDD
134 GNDD
135 GNDD
136 GNDD
137 GNDD
138 GNDD
139 GNDD
140 GNDD
141 GNDD
142 VREFP
143 VREFP
144 GNDD_REF
145 REXTD
146 VDD_SHUNT
147 EXT_POR_CAP
148 DATA_IN0_P
149 DATA_IN0_N
150 DATA_IN1_P
151 DATA_IN1_N
152 DATA_IN2_P
153 DATA_IN2_N
154 DATA_IN3_P
155 DATA_IN3_N
156 GPO_LVDS0_N
157 GPO_LVDS1_N_P
158 GPO_LVDS1_P_N
159 GPO_LVDS2_P
160 GPO_LVDS2_N
161 GPO_LVDS3_P
162 GPO_LVDS3_N
163 NC3
164 GNDA
165 GNDA
166 GNDA
167 GNDA
168 GNDA
169 VDDA
170 VDDA
171 VDDA
172 VDDA
173 VDDA
174 VINA
175 VINA
176 VINA
177 VINA
178 VINA
179 VIND
180 VIND
181 VIND
182 VIND
183 VIND
184 VDDD
185 VDDD
186 VDDD
187 VDDD
188 VDDD
189 GNDD
190 GNDD
191 GNDD
192 GNDD
193 GNDD
194 VSUB
195 VDD_EFUSE
196 DET_GRD1