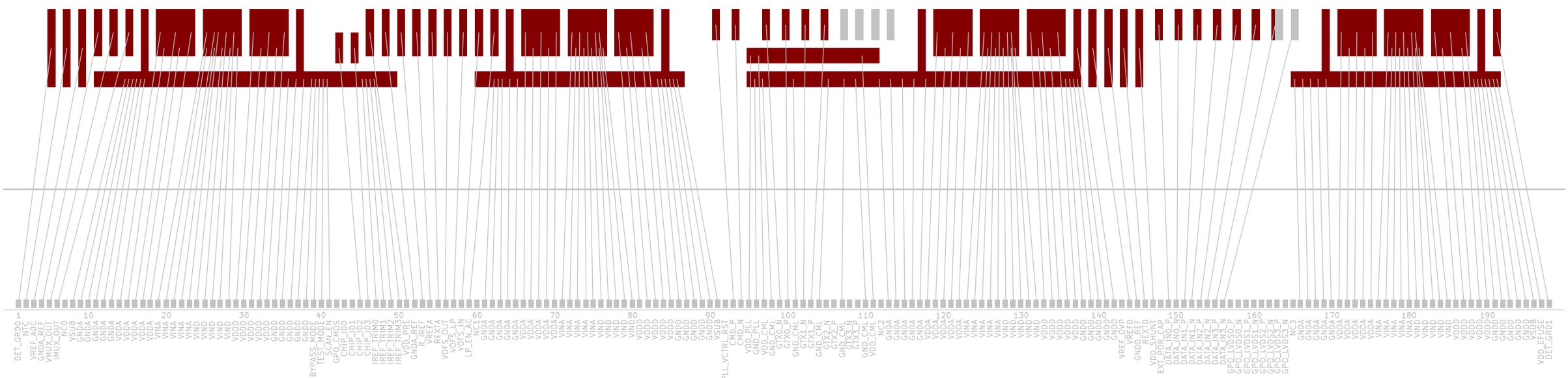
RD53B


1 DET_GRD0
2 NTC
3 VREF_ADC
4 GNDA_REF
5 VMUX_OUT
6 IMUX_OUT
7 NTCG
8 VSUB
9 GNDA
10 GNDA
11 GNDA
12 GNDA
13 VDDA
14 VDDA
15 VDDA
16 VDDA
17 VINA
18 VINA
19 VINA
20 VINA
21 VIND
22 VIND
23 VIND
24 VIND
25 VDDD
26 VDDD
27 VDDD
28 VDDD
29 VDDD
30 GNDD
31 GNDD
32 GNDD
33 GNDD
34 GNDD
35 BYPASS_MODE
36 TEST_MODE
37 SCAN_EN
38 GPO_CMOS
39 CHIP_ID0
40 CHIP_ID1
41 CHIP_ID2
42 CHIP_ID3
43 IREF_TRIM0
44 IREF_TRIM1
45 IREF_TRIM2
46 IREF_TRIM3
47 VDD_PRE
48 GNDA_REF
49 R_REF
50 VREFA
51 VREFD
52 VOFS_OUT
53 VOFS_IP
54 VOFS_IN
55 LP_EN_AC
56 NC1
57 GNDA
58 GNDA
59 GNDA
60 GNDA
61 GNDA
62 VDDA
63 VDDA
64 VDDA
65 VDDA
66 VINA
67 VINA
68 VINA
69 VINA
70 VINA
71 VINA
72 VIND
73 VIND
74 VDDD
75 VDDD
76 VDDD
77 VDDD
78 VDDD
79 GNDD
80 GNDD
81 GNDD
82 GNDD
83 GNDD
84 VSUB
85 PLL_VCTRL_RST
86 CMD_P
87 CMD_N
88 VDD_PLL
89 GND_PLL
90 VDD_CML
91 GND_CML
92 GIXO_P
93 GIXO_N
94 GND_CML
95 GIX1_N
96 GIX1_P
97 GND_CML
98 GIX2_N
99 GIX2_P
100 GND_CML
101 GIX3_N
102 GIX3_P
103 GND_CML
104 VDD_CML
105 NC2
106 GNDA
107 GNDA
108 GNDA
109 GNDA
110 GNDA
111 VDDA
112 VDDA
113 VDDA
114 VDDA
115 VINA
116 VINA
117 VINA
118 VINA
119 VIND
120 VIND
121 VIND
122 VIND
123 VDDD
124 VDDD
125 VDDD
126 VDDD
127 VDDD
128 GNDD
129 GNDD
130 GNDD
131 GNDD
132 GNDD
133 GNDD
134 GNDD
135 GNDD
136 GNDD
137 VREFP
138 VREFP
139 GNDD_REF
140 REXTD
141 VDD_SHUNT
142 EXT_POR_CAP
143 DATA_IN0_P
144 DATA_IN0_N
145 DATA_IN1_P
146 DATA_IN1_N
147 DATA_IN2_P
148 DATA_IN2_N
149 DATA_IN3_P
150 DATA_IN3_N
151 GPO_LVDS0_N
152 GPO_LVDS0_P
153 GPO_LVDS1_N
154 GPO_LVDS1_P
155 GPO_LVDS2_N
156 GPO_LVDS2_P
157 GPO_LVDS3_N
158 GPO_LVDS3_P
159 NC3
160 GNDA
161 GNDA
162 GNDA
163 GNDA
164 GNDA
165 VDDA
166 VDDA
167 VDDA
168 VDDA
169 VINA
170 VINA
171 VINA
172 VINA
173 VIND
174 VIND
175 VIND
176 VIND
177 VDDD
178 VDDD
179 VDDD
180 VDDD
181 GNDD
182 GNDD
183 GNDD
184 GNDD
185 GNDD
186 VSUB
187 VDD_GRD1
188 DET_GRD1